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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Terunao HANAOKA et al.

Group Art Unit: 2818

Application No.: 10/768,168

Examiner: T. Le

Filed: February 2, 2004

Docket No.: 109690.01

For: SEMICONDUCTOR DEVICE, METHOD OF FABRICATING THE SAME, STACK-TYPE SEMICONDUCTOR DEVICE, CIRCUIT BOARD AND ELECTRONIC INSTRUMENT

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In reply to the February 8, 2005 Office Action, please consider the following:

Amendments to the Claims as reflected in the listing of claims; and

Remarks.